



**MICROTEK**  
IN-CIRCUIT EMULATORS

## PowerPack® EA-Am186 In-Circuit Emulator

- *Microtek exclusive clock-edge resolution trace and trigger system*
- *128-bit wide trace, 1 MB depth with PowerPack EA, 128K depth with PowerPack SW*
- *Processor speeds to 40 MHz*
- *Four complex events, four sequential levels per event*
- *Easy to use, 100% Windows® source-level debugger emulator interface*
- *Unlimited hardware breakpoints*
- *Eight external trace inputs*
- *Includes 1 MB overlay memory and target adapter*
- *Small footprint, 7.1" x 3.7" x 1.1"*
- *One system supports all Am186/188 versions – EM, ER, ES, ED*
- *Complete range of adapters for PQFP or TQFP (clip-on and solder-on)*
- *World-wide sales and support organization*

This new high-performance emulator for Am186™ design combines an easy to use source-level debugger and a powerful trace and event system. It can trigger and capture data with clock-edge resolution allowing you to identify and solve the toughest software and hardware bugs that are invisible to other in-circuit emulators and software debuggers.

### 128-Bit Wide Trace, 1 MB Depth with Clock Trace Option

Most emulators collect trace on just a few processor status signals along with the usual address and data signals. PowerPack has the industry's widest trace buffer. It can trigger and trace on all control and peripheral signals, a great benefit when debugging the highly integrated Am186 processors. No other emulator comes close. PowerPack SW has 128K trace depth. PowerPack EA with clock trace has 1 MB trace depth. This makes both emulators ideal for data-intensive applications like telecommunications.

### Clock-edge Resolution

*A Microtek exclusive...* clock-edge resolution on trace and triggers means the emulator can trigger and collect trace on clock cycle events in addition to the usual bus cycles. Ready hangs, incorrect Holds or DMA cycles are examples of target debug problems that only clock-level triggers and trace can solve.



### Advanced Event Triggers

Define up to four events on address, data, processor status, I/O and peripheral signals. Ranges, bit masking and negation apply to both address and data. Combine these with two 32-bit counter/timers to set four sequential triggers for break and trace. Events and triggers can be set or modified and trace can be viewed without stopping the target system.

### 100% Windows Interface

Control execution with go, halt, goto cursor, go until/into call, return, step over, single step by source line or instruction. Set breakpoints at the source level. The variable window continuously monitors global or stack variables. Easily view and modify memory and peripheral registers.

The PowerPack EA-Am186 emulator is ultra compact. It runs on Windows®3.1, 95 and NT, and supports the latest compilers from Borland, Microsoft, and CAD-UL. PowerPack emulators are also available for the Am386®DX/SX and Am486®DX.

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